

Based on Form PTO-1449
(3/90)

ATTY. DOCKET NO.

790001-2034

SERIAL NO.

10/628,689

~~Not Yet Assigned~~

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANTS

Noriaki MATSUNAGA et al.

FILING DATE

July 28, 2003

GROUP

2814

~~Not Yet Assigned~~

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

HP				K. Higashi, et al., "A Manufacturable Copper/Low-k SiOC/SiCN Process Technology for 90nm-node High Performance eDRAM," PROCEEDINGS OF THE IEEE 2002 INTERNATIONAL INTERCONNECT TECHNOLOGY CONFERENCE, June 2002, pp. 15-17.				

EXAMINER

Uoaylam

DATE CONSIDERED

09/15/04

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.